

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination GOETZINGER ET AL.	
		Examiner Deepak Soni	Art Unit 2666	Page 1 of 1

U.S. PATENT DOCUMENTS

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
A	US-6,481,251	11-2002	Meier et al.	70/52
B	US-6,810,043	10-2004	Naven et al.	370/412
C	US-			
D	US-			
E	US-			
F	US-			
G	US-			
H	US-			
I	US-			
J	US-			
K	US-			
L	US-			
M	US-			

FOREIGN PATENT DOCUMENTS

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
N					
O					
P					
Q					
R					
S					
T					

NON-PATENT DOCUMENTS

*	Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages
U	"Estimating Clock Speeds for the ATMSWITCH Architecture", Proc. NETWORKS '99 (The Third New Zealand ATM and Broadband Workshop), 21-22 Jan 1999, pps 39-53: Lyons, P.J. and Yan, Bo (1999)
V	
W	
X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.